



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: YONG-NIEN RAO ET AL

SERIAL NO.: 10/799,675

GROUP ART UNIT: 2629

FILED: March 15, 2004

EXAMINER: Henry N. TRAN

FOR: TIMING GENERATOR OF FLAT PANEL DISPLAY
AND POLARITY ARRANGEMENT CONTROL
SIGNAL GENERATION METHOD THEREFOR

ATTY. REFERENCE: RAOY3002/BEU

COMMISSIONER OF PATENTS

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

The below identified communication(s) or document(s) is(are) submitted in the above application or proceeding:

- | | |
|--|---|
| <input type="checkbox"/> Declaration | <input checked="" type="checkbox"/> Issue Fee Transmittal - Part B |
| <input type="checkbox"/> Priority Document | <input checked="" type="checkbox"/> Check - \$1740 (\$1440 - IF/\$300-Pub. Fee) |
| <input checked="" type="checkbox"/> 2- Formal Drawings (Figs. 5 & 8) | <input type="checkbox"/> Application Data Sheet |
| | <input type="checkbox"/> Assignment |

☒ Please debit or credit **Deposit Account Number 02-0200** for any deficiency or surplus in connection with this communication. A duplicate copy of this sheet is provided for use by the Deposit Account Branch.

☐ Small Entity Status is claimed.

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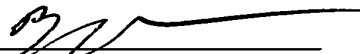
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DATE: October 23, 2007

Respectfully submitted,



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